

10-08-03

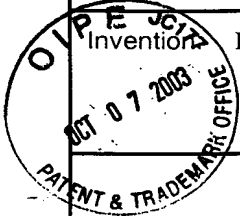
EL 983170185US

2133

**AMENDMENT TRANSMITTAL LETTER (Large Entity)**Applicant(s): **Richard Illman**

Docket No.

CAF-27202/03

Serial No.  
09/808,325Filing Date  
3/14/2001Examiner  
Marshall S. EngGroup Art Unit  
2133**IMPROVED METHOD FOR TESTING FOR THE PRESENCE OF FAULTS IN DIGITAL CIRCUITS****RECEIVED****OCT 22 2003****Technology Center 2100**TO THE COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

**CLAIMS AS AMENDED**

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	7 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	1 -	3 =	0 x	\$86.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_
- ☐ A check in the amount of \_\_\_\_\_ to cover the filing fee is enclosed.
- ☒ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. **07-1180**
- ☒ Any additional filing fees required under 37 C.F.R. 1.16.
- ☒ Any patent application processing fees under 37 CFR 1.17.

Dated: **October 7, 2003**

**Judith M. Riley**  
**Registration No. 31,561**  
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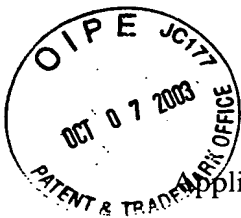
I certify that this document and fee is being deposited on \_\_\_\_\_ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

\_\_\_\_\_  
*Signature of Person Mailing Correspondence*

\_\_\_\_\_  
*Typed or Printed Name of Person Mailing Correspondence*

CC:

#5A/10-28-03  
v. Serey



Attorney Docket No. CAF-27202/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Richard Illman

Serial No.: 09/808,325

Group Art Unit: 2133

Filing Date: March 14, 2001

Examiner: Marshall S. Eng

For: IMPROVED METHOD FOR TESTING FOR THE  
PRESENCE OF FAULTS IN DIGITAL CIRCUITS

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed July 7, 2003, please amend the above-identified patent application in the following manner:

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**OCT 22 2003**  
**Technology Center 2100**